

Analog/Mixed-Signal IC Design Engineer

HONG KONG OR SHENZHEN, FULL-TIME REGULAR

Analog/Mixed-Signal Design Engineer works on high-performance analog-to-digital converter (ADC) and digital-to-analog converter (DAC). The successful candidate in this role will do transistor-level circuit design, layout design, silicon evaluation, and characterization to final product until mass production.

Qualifications

- MS with 2+ years of work experience in high-performance analog/mixed-signal/RF IC design and development in deep submicron CMOS processes
- Experience in 28nm HPC, 16nm FFC/FF+ or below will be highly preferable
- Familiar with high-resolution, high-speed ADC and DAC data converter design knowledge and techniques
- Understanding design issues of building blocks such as precision bandgap reference and buffer, linear regulators, amplifiers, comparators, or oscillators
- Good understanding noise and mismatch issues in transistor-level circuit design
- Experience in silicon evaluation on laboratory, and familiar with the use of standard test equipment
- Familiar with Cadence design environment, mixed-signal circuit simulation tools, layout verification and parasitic extraction tools
- Understanding design techniques for aging, electromigration, ESD, substrate noise in deep submicron CMOS process is a plus
- Must be able to work independently according to schedules and be team player
- Should be proactively seeking for help and share knowledge to team members
- Strong written and verbal communication skills